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**Today's Date: January 6, 2006**

**To: Examiner Y. Rossoshek, Art Unit: 2825**

**FAX: (571) 273-8300**

**From: Frederick E. Cooperrider #36,769  
McGinn Intellectual Property Law Group, PLLC  
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**In re Application of Sato, et al**

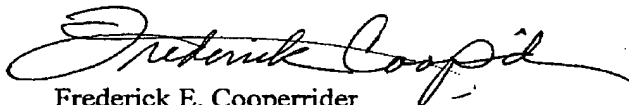
**Serial No.: 10/623,575**

**For: APPARATUS AND METHOD FOR ESTIMATING POWER CONSUMPTION**

**Contents: 1. Statement of Substance of Interview (2 pages)**

**CERTIFICATION OF TRANSMISSION**

I certify that I transmitted via facsimile to (571) 273-8300 this Statement of Substance of Interview to Examiner H. Rossoshek on January 6, 2006.



Frederick E. Cooperrider  
Reg. No. 36,769

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of

Koichi Sato, et al.

Serial No.: 10/623,575

Group Art Unit: 2825

Filed: July 22, 2003

Examiner: Y. Rossoshek

For: APPARATUS AND METHOD FOR ESTIMATING POWER CONSUMPTION

Honorable Commissioner of Patents  
Alexandria, VA 22313-1450**STATEMENT OF SUBSTANCE OF INTERVIEW**

Sir:

In response to the requirement that a statement of the substance of an interview be placed in the record, Applicants hereby submit the following.

Applicants gratefully acknowledge Examiner Rossoshek and Primary Examiner Thompson for taking time from their busy schedules to conduct a personal interview on January 3, 2006, for the above-referenced Application. The interview was courteous and professional, and it is believed by Applicants' representative that prosecution has been advanced because of this interview.

Concerning the substance of the interview, Applicants' representative explained that the present invention differed from conventional methods by including the capability to use either a register or a memory unit and to use the much more rapid clock-based simulation for calculating the power consumption of the specific unit.

Examiner Rossoshek indicated that she interpreted that item 32 in Figure 4 of Ikegami provided this capability but that possibly there was allowable subject matter if claim 2 were to be incorporated into claim 1 and various phrases in the claims were explained better.

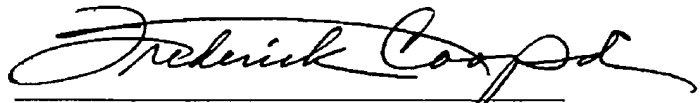
Serial No. 10/623,575  
Docket No. NEG-299US (KATO.029)  
Interview Summary

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Applicants' representative explained that he had not interpreted the description in the text of item 32 of Figure 4 as describing the capability of selecting either a register structure or a memory structure based on the behavioral synthesis information (e.g., because of the wording of lines 21-22 of column 7: "The model I/O section 32 has an I/O register structure and an I/O memory structure."), but that he would give more thought to the Examiner's position in responding to the next Office Action, as well as attempt to find wording more acceptable to the Examiner for those phrases in the claims that the Examiner identified as causing concern.

1/6/06

Date



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